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RESPONSE UNDER 37 C.F.R.
§1.116 EXPEDITED PROCEDURE
EXAMINING GROUP 2814

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Steven J. Koester

Examiner: Anh D. Mai

Serial No: 10/717,279

Art Unit: 2814

Filed: November 19, 2003

Docket: YOR920030533US1 (17110)

For: SEMICONDUCTOR FIELD-EFFECT
TRANSISTOR HAVING STRAINED-LAYER
(As Amended)

Dated: June 26, 2006

Confirmation No: 7401

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Commissioner of Patents and Trademarks
P.O. Box 1450
Alexandria, Virginia 22313-1450

RESPONSE UNDER 37 C.F.R. §1.116

Sir:

In response to the Final Rejection dated April 26, 2006, applicants respectfully request the Examiner to reconsider the application in view of the following amendments and remarks.

Amendment to the Title is reflected in the amendment provided on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.

Remarks begin on page 8 of this paper.

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that this paper is being facsimile transmitted to the Patent and Trademark Office on the date shown below.

Dated: June 26, 2006


Steven Fischman